

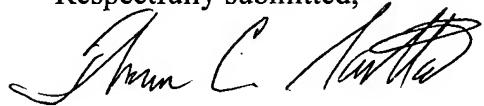
REMARKS

The subject matter of the Section 112 objections has been addressed by deleting language from the original abstract and correcting the terminology in claims 11 through 17. It is respectfully submitted that the language of claims 5 and 6 are not substantial duplicates in that the operation does not produce identical values for the difference signal.

The claims have been amended to properly define the invention, in that the step of integrating the first half-symbol signal component of a unit bit cell over a half-symbol period to produce a first half-signal component value and integrating the second half-symbol signal component of a unit bit cell over a half-symbol period to produce a second half-signal component value is critical to the invention. Likewise, in the article claims, devices that accomplish this task are now described. In the prior art, as demonstrated by the Saltzberg '059 reference, systems sample a single value at a single point of time during any given half-symbol period. Depending on the particular moment in time chosen, noise factors may significantly affect the sampled value, leading to the possibility of obtaining a false sample value. A truer value is obtained by integrating over the half-symbol period, since the full energy of the half-symbol period is determined and the noise effects will tend to cancel themselves out. There is no teaching in Saltzberg '059 or other known prior art that anticipates or makes obvious the invention as now claimed. Furthermore, the Saltzberg '059 system will not work with modulated signals, whereas the invention at hand will work with modulated signals.

It is respectfully submitted that the claims as presented are patentable, on the basis of the above remarks, and reconsideration and subsequent passage for allowance is hereby requested.

Respectfully submitted,



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